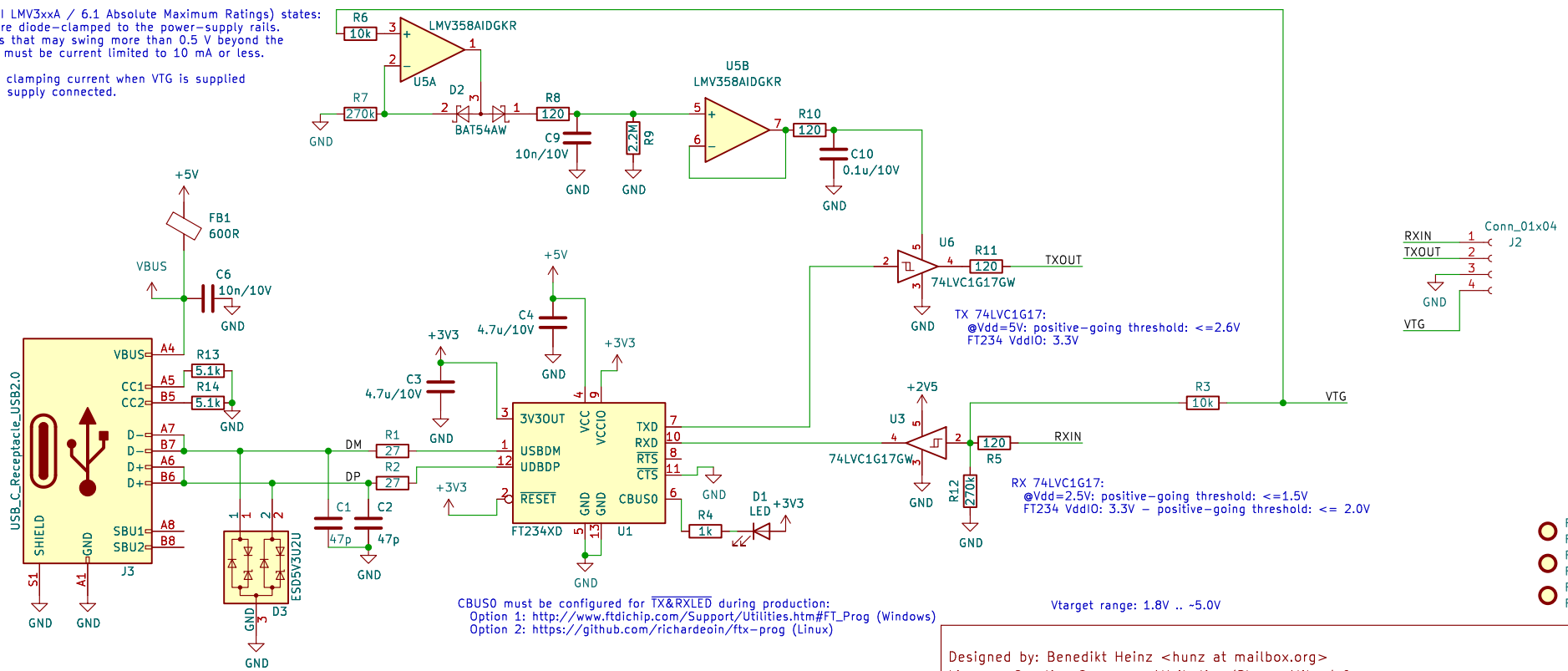


* Vtg tracking through Rx peak detector based on <https://www.analog.com/en/technical-articles/ltc6244-high-speed-peak-detector.html>
 * Designed for 4.8kBaud .. 250kBaud, 1.8V .. 5V
 * R9 chosen to power down output -30ms after Rx goes low

SBOS923H (TI LMV3xxA / 6.1 Absolute Maximum Ratings) states:
 Input pins are diode-clamped to the power-supply rails.
 Input signals that may swing more than 0.5 V beyond the supply rails must be current limited to 10 mA or less.
 R6 limits the clamping current when VTG is supplied with no USB supply connected.



CBUS0 must be configured for TX&RXLED during production:
 Option 1: http://www.ftdichip.com/Support/Utilities.htm#FT_Prog (Windows)
 Option 2: <https://github.com/richardeoin/ftx-prog> (Linux)

Vtarget range: 1.8V .. -5.0V

Designed by: Benedikt Heinz <hunz at mailbox.org>
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Sheet: /	
File: usb2serial.kicad_sch	
Title: USB2serial AutoVtg	
Size: A4	Date: 2023-07-28
KiCad E.D.A. kicad 7.0.8-7.0.8-ubuntu22.04.1	Rev: v2
	Id: 1/1

- FID1
- FID2
- FID3
- FID4