C0-Nano Pinout

rev 1
10.03.2025

LEDs: 3V3 PB2 PB1

T14CH1 T3CH4 (PWM)

3.3V ·· connect during powerup
PA14 SWCLK BOOT0 ········· for bootloader
PA13 SWDIO
GND

T3ETR T1CH2 SCL1 U1TX MISO2 PA9
T1CH3 SDA1 U1RX MOSI2 PA10
PF2/nRST
GND

T16CH1 T3CH1 SCL1 SCK2 PB8
T3CH3 nSS1 PB0
T3CH3 T1ETR PC15
U1TX T1ETR PC14
T2CH3 T3CH1 PC6
T17CH1 T3CH2 SDA1 nSS2 PB9
T3CH3 T1CH3 SCK1 MISO1 MOSI1 SCL1 U1TX MISO2 PB6
T3CH4 T1CH4 T16CH1 T3CH1 SCL1 SDA1 U1RX MOSI2 PB7
T2ETR T2CH1 T1CH1 U2RX nSS1 PA15
T3CH2 T3CH3 MOSI1 PB5
T3CH1 SDA2 MISO1 PB4

unused
GND
PF2/nRST
5V
+ T3CH3 T3CH4 T14CH1

PA8 ADC8 nSS2 U2TX MISO2 nSS1 U1RX T1CH1
PA7 ADC7 MOSI1 SCL2 T17CH1 T3CH2 T14CH1
PA6 ADC6 MISO1 SDA2 T16CH1 T3CH1
PA5 ADC5 SCK1 U2RX T1CH1 T2CH1 T2ETR
PA4 ADC4 nSS1 U2TX MOSI2 T14CH1
PA3 ADC3 MISO2 U2RX T1CH4 T2CH4
PA2 ADC2 MOSI1 U2TX T3ETR T1CH3 T2CH3
PA1 ADC1 SCK1 U1RX T17CH1 T1CH2 T2CH2
PA0 ADC0 SCK2 U1TX T16CH1 T1CH1 T2CH1 T2ETR
3.3V
PB3 SCK1 SCL2 T1CH2 T2CH2 T3CH2

PA11
PA12
USB (Full-Speed)

MCU: STM32C071

all IOs are 3.3V (but 5V-tolerant)
and interrupt-capable
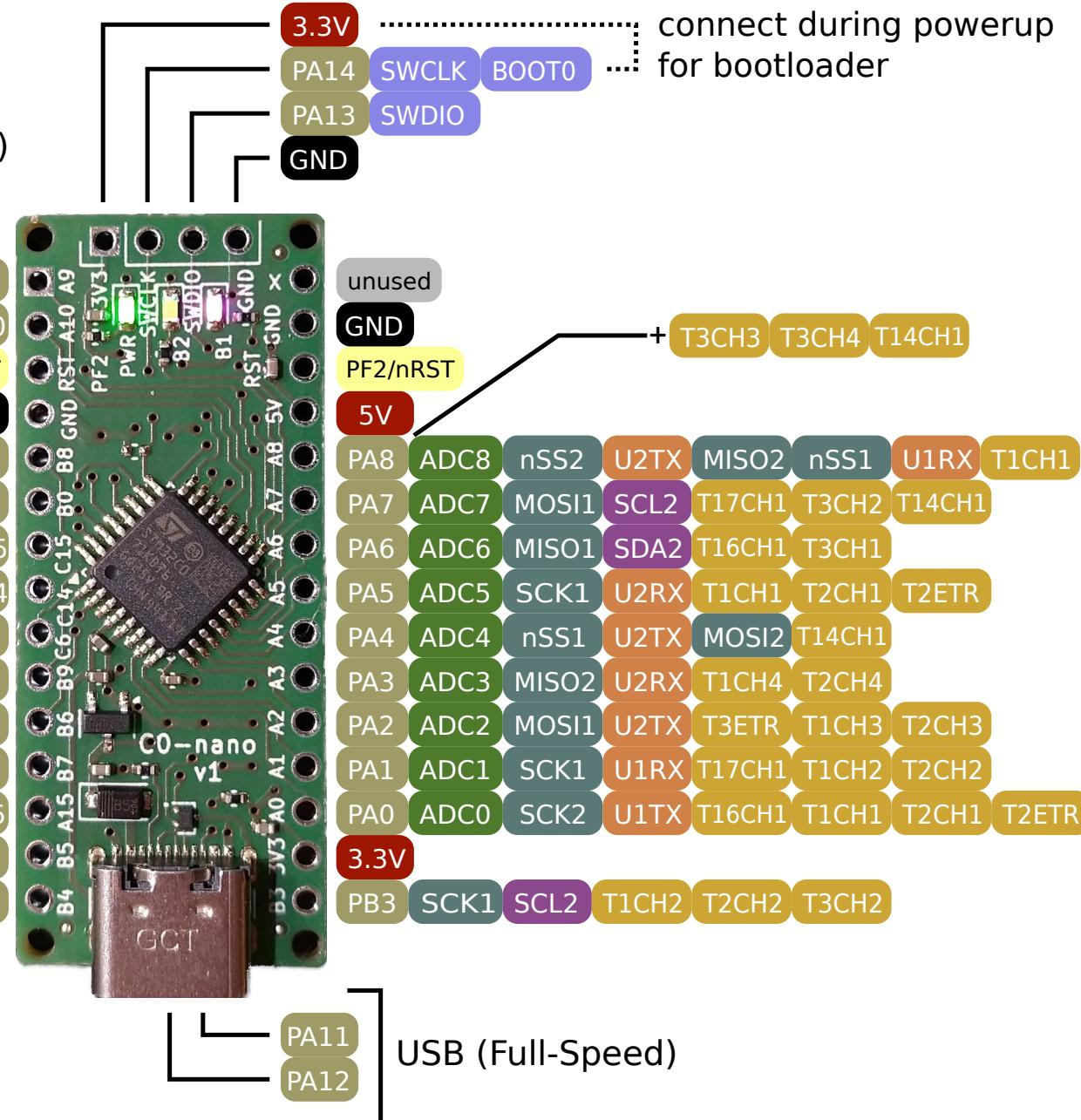
See STM32C071 datasheet for additional pin functions

znu.nz/c0

Board silkscreen labels: A9 3V3 PWR SWCLK B2 SWDIO B1 GND ×
RST A10 · RST · GND · 5V
PF2 · A8
B0 RST · A7
GND · A6
B8 · A5
C15 · A4
C14 · A3
C6 · A2
B9 · A1
B6 · A0
B7 · 3V3
A15 · B3
B5
B4

C0-nano v1
GCT